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Sheet 1 of 2

Form PTO-1449 U.S. DEPARTMENT OF COMMERCE (REV. 7-80) PATENT AND TRADEMARK OFFICE		Atty. Docket No. US010142 (702107)		Serial No. To be assigned		
INFORMATION DISCLOSURE CITATION (Use several sheets if necessary)		Applicant Monisha Ghosh		Filing Date Herewith		
		Group Unassigned				
U.S. PATENT DOCUMENTS						
Ex. Int	Document Number	Date	Name	Class	Sub-class	Filing Date If Approp.
AA						
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FOREIGN PATENT DOCUMENTS						
	Document Number	Date	Country	Class	Sub-class	Trans. Yes No
AG						
AH						
AI						
AJ						
AK						
OTHER (Including Author, Title, Date, Pertinent Pages, Etc.)						
RW	AL	Meikkilä, M.J., et al., "Interference Suppression in CDMA Downlink through Adaptive Channel Equalization", IEEE, pp. 978-982, 1999				
	AM	Komulainen P., et al., "Adaptive Channel Equalization and Interference Suppression for CDMA Downlink", IEEE 6 th Int. Symp. on Spread-Spectrum Tech. & Appl., NJIT, NJ, USA, pp. 363-367, Sept. 6-8, 2000				
✓	AN	Tsatsanis, M.R., "Inverse Filtering Criteria for CDMA Systems", IEEE Transactions on Signal Processing, Vol. 45, No. 1, pp. 102-112, January 1997				
Examiner /Robert Wilson/			Date Considered 07/06/2006			
*EXAMINER: Initial if reference considered, whether or not citation is in conformance with 609; Draw line through citation if not in conformance and not considered. Include a copy of this form with next communication to applicant.						

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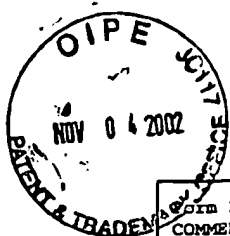
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RW	AL	Caire G., et al., "Pilot-aided Adaptive MMSE Receivers for DS/CDMA", IEEE, pp. 57-62, 1999
↓	AM	Klein, A., "Data Detection Algorithms Specially designed for the Downlink of CDMA Mobile Radio Systems", IEEE, pp. 203-207, 1997
↓	AN	Krauss, T.P., et al., "Mimse Equalization for Forward Link in 3G CDMA: Symbol-Level Versus Chip-Level", IEEE, pp. 18-22, 2000
↓	AO	Hooli, K., et al., "Multiple Access Interference Suppression with Linear Chip Equalizers in WCDMA Downlink Receivers", Global Telecommunications Conference - Globecom'99, pp. 467-471

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Form PTO-1449 COMMERCE (REV. 7-80)		U.S. DEPARTMENT OF PATENT AND TRADEMARK OFFICE		Atty. Docket No. US 010142		Serial No. 09/978,118								
INFORMATION DISCLOSURE CITATION (Use several sheets if necessary)				Applicant MONISHA GHOSH										
				Filing Date 10/15/01		Group 2681								
U.S. PATENT DOCUMENTS														
Ex. Int.		Document Number						Date	Name	Class	Sub-class	Filing Date If Approp.		
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FOREIGN PATENT DOCUMENTS														
		Document Number						Date	Country	Class	Sub-class	Trans. Yes No		
	AG													
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RW	AJ	Ghosh: "Adaptive chip-equalizers for synchronous DS-SS/CDMA systems with pilot sequences" GLOBECOM'01, 25-29 November 2001, pages 3385-3389												
	AK	Hooli et al: "Performance Evaluation of Adaptive Chip-Level Channel Equalizers in WCDMA Downlink" IEEE International Conference On Communications (ICC) 01, 11-14 June 2001, pages 1974-1979												
	AL	Frank et al: "Adaptive Interference Suppression for Direct-Sequence CDMA Systems with Long Spreading Codes" 36 th Annual Allerton Conference On Communication, Control And Computing, 23-25 September 1998, pages 411-420												
	AM	Komulainen et al: "Adaptive channel equalization and interference suppression for CDMA downlink" IEEE ISSSTA. IEEE International Symposium On Spread Spectrum Techniques And Applications, vol. 2, September 6, 2000, pages 363-367												
	AN	Hooli et al: "Multiple Access Interference Suppression With Linear Chip Equalizers In WCDMA Downlink Receivers" 1999 IEEE Global Telecommunications Conference, New York, NY: IEEE, US, vol. 1A, 5 December 1999, pages 467-471												
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